Dhruv Dhawan

Software Developer | Full Stack Developer | IT Consultant

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EDUCATION

York University   **Toronto, ON**
Bachelors of Engineering with honors in Computer Engineering, May 2021
- Specialized Honors Bachelor of Engineering (Honors BEng)
- Related coursework: Computer Architecture, Digital Communications, Software Engineering Requirements, Digital Logic Design (VHDL), Relational Databases.

WORK EXPERIENCE

TD Bank Group   **Toronto, ON**
Quality Engineer   September 2022 - October 2024
- Identified and resolved duplicate report ordering, resulting in annual cost savings of over $1.2M for TD.
- Increased rate of automation through automation through Underwriting Rules with an improvement of 0.25%. Projected earnings with this change in FTE savings, Productivity and Revenue Growth goals with estimated 5-year benefit amount of $1M-$5M
- Contributed to test planning and performed hands-on test design, development, maintenance, test case automation, and test execution to achieve business outcomes.
- Analyzed end-to-end proposed solutions (business requirements, design specifications, architecture blueprints) to develop in-scope tests and maintain test suites (automated/manual).
- Provided quick feedback throughout the development and design process, analyzing and reporting on test results to ensure the quality of the intended solution.
- Implemented functional and non-functional automation (performance, readiness/reliability testing, accessibility, security).
- Designed, executed, and maintained comprehensive test plans, test scenarios, and test cases for functional, regression, integration, performance testing, and time-based testing of PolicyCentre, BillingCentre enhancements in Guidewire.

FDM Group Ltd.,   **Toronto, ON**
IT Consultant   June 2022 - October 2024
- Provided accurate feedback to developers and performed thorough system testing, identifying, and resolving bugs.
- Collaborated with cross-functional teams, to improve process efficiency and accuracy Investigated root causes of quality issues and collaborated with IT teams to implement technological solutions, maintaining defect rate in production to a minimum.
- Collaborated in an Agile work environment, in a pod including developers, business analysts, and Product Owner, to define quality standards and testing methodologies for claims enhancement initiatives
- Utilized automated testing tools (Selenium, Java, Cucumber) to streamline testing processes, resulting in a reduction in testing time and improved accuracy

LEADERSHIP EXPERIENCE

President for the Electrical Engineering Club for Students (EECS) (Sept2017-Nov2018)
Organized meet and greets, workshops and tutorials for the club. Co-constructed a transmitter device to send signals for demonstration purposes, taught signals basics to first and second-year students.

Orientation Leader for Lassonde School of Engineering (2017, 2018)
Organized meet and greet, team-building and networking events for successful transition of 350+ incoming students to the Lassonde School of Engineering. Showed students where to contact each department for any queries that they might have.

Volunteer Researcher at the York University Observatory (Oct 2017 – Jan 2018)
Set up 60 cm telescope and collected data on pulsing stars for research. Utilized a sensitive charged-couple device (CCD) for collecting the data on the stars.

Attended the Engineers Without Borders Conference on Sustainable Development (Xchange) (Jan 2019)
Interacted with likeminded peers and exchanged ideas to develop solutions to the world's most pressing sustainability challenges.

PROJECTS

Hospital Communication System: Developed a web application for an enhanced communication channel between nurses and physicians.
Tech used: React, Express SQL, Postman, figma, Node.js.

Weather App: Developed a weather app that provides user current weather details of the city requested, for cities with same name, country code can be specified.
Tech used: HTML, CSS, JS, figma.

Traffic Light System: Created a traffic light controller using an FPGA. Made finite state machines.
Tech used: Verilog, Quartus II, Alterra DE II board, Qsim.

SKILLS & INTERESTS

Skills: Microsoft Office (Excel, PowerPoint) | JavaScript| HTML| CSS| C| Verilog (VHDL)| SQL| C++| MATLAB| Java| Python

Interests: Sketching, reading, and playing various types of sports.